Confidential Document

DATA IMAGE CORPORATION

LCD Module Specification

ITEM NO.: **CXM20211SR-JA**

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R&D Dept.	Q.C. Dept.	Eng. Dept.	Prod. Dept.
Version:	Issued Date:	Sheet Code:	Total Pages:
C	2001/1/12		24

2. RECORD OF REVISION

Rev	Date	Item	Page	Comment
B	SEP/04/00	15	23	Correct Fixing hole dimension.
C	JAN/12/01	1,13	21	1. Change LCD Supplier to JA Code 2. ADD DRIVER IC AX6112H.

3. GENERAL SPECIFICATION

Display Format :	20 characters (W)	×	2 lines (H)	
Character Size :	3.2 (W)	×	5.55 (H)	mm
View Area :	76.7 (W)	×	15.2 (H)	mm
General Dimensions :	86.9 (W)	×	33.4 (H) ×	15 (T) mm Max.
Weight :	30 g max.			

LCD Type : [V] STN Gray [ ] STN Yellow [ ] FSTN [ ] TN

Polarizer mode : [V] Reflective [ ] Transflective

[ ] Transmissive [ ] Negative

View Angle : [V] 6 O'clock [ ] 12 O'clock [ ] Others ____

Backlight : [ ] LED [ ] EL [ ] CCFL

Backlight Color : [ ] Yellow green [ ] Amber [ ] Blue Green

[ ] White [ ] Others

Controller / Driver : KS0066

Temperature Range : [V] Normal [ ] Wide Temperature

Operating	0 to 50°C	Operating	-20 to 70°C
Storage	-20 to 70°C	Storage	-30 to 80°C

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

V_{SS}= 0V, Ta = 25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	V_{DD}-V_{SS}	0	7	V
Supply Voltage (LCD Driver)	V_{DD}-V_0	1.5	13.5	V
Input Voltage	V_I	V_{SS}	V_{DD}	V
Operating Temperature	TOP	0	50	°C
Storage Temperature	TSTG	-20	70	°C

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

Item	Operating		Storage		Comment
	(Min.)	Max.)	(Min.)	(Max.)	
Ambient Temp	0	50	-20	70	Note (1)
Humidity	Note (2)		Note(2)		Without Condensation
Vibration	--	4.9M/S^2	--	19.6M/S^2	XYZ Direction
Shock	--	29.4M/S^2	--	490M/S^2	XYZ Direction

Note(1) Ta = 0°C : 50Hr Max.

Note(2) Ta ≤ 40°C : 90% RH Max.

Ta ≥ 40°C : Absolute humidity must be lower than the humidity of 90% RH at 40°C•••

5. ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply Voltage (Logic)	V_{DD}-V_{SS}		2.7	5.0	5.5	V
Supply Voltage (LCD)	V_{DD}-V_O	0°C	4.3	4.7	4.9	V
		25°C	4.2	4.5	4.8	
		50°C	3.6	3.9	4.3	
Input Voltage	V_{IH}	--	0.7*V_{DD}	--	V_{DD}	V
	V_{IL}		V_{SS}	--	0.3*V_{DD}	
Logic Supply Current	I_{DD}	V_{DD}-V_{SS}=5V	1.5	1.5	1.6	mA

6. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	Symbol	Condition	Min.	Typ.	Max.	Unit	Ref.
Rise Time	Tr	0°C	--	700	1050	ms	Note (1)
		25°C		180	270		
Fall Time	Tf	0°C	--	1800	2700	ms	
		25°C		200	280		
Contrast	CR	25°C		5			Note (3)
View Angle	θ1~θ2	25°C &	30	--	--		Note (2)
	Ø1, Ø2	CR≥3	30	--	--		
Frame Frequency	Ff	25°C	--	70	--	Hz	

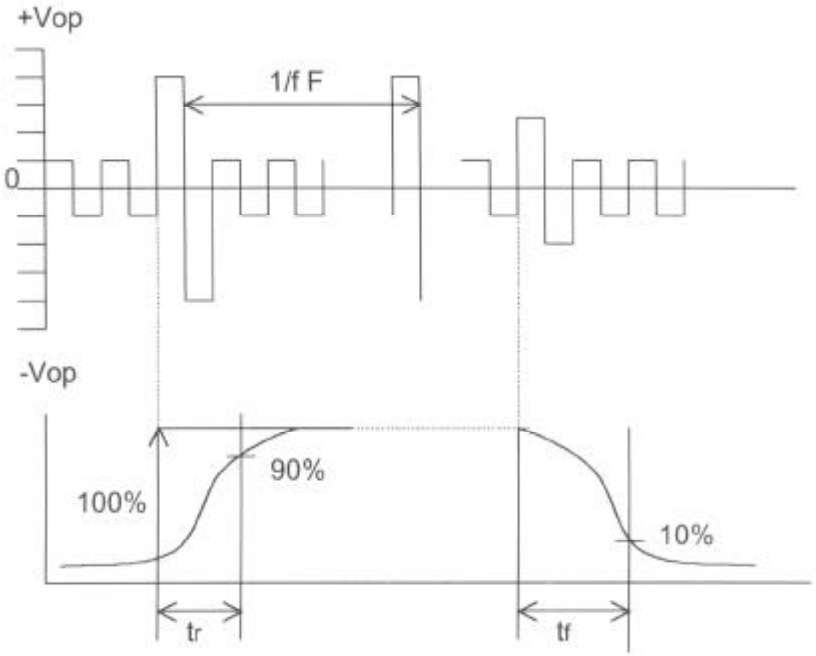
Note (1) & (2) : See next page

Note (3) : Contrast ration is defined under the following condition:

$$CR = \frac{\text{Brightness of non-selected condition}}{\text{Brightness of selected condition}}$$

(a). Temperature ---------- 25°C
(b). Frame frequency ---- 70Hz
(c). Viewing angle -------- θ= 0°, •∅ = 0°
(d). Operating voltage --- 4.5V

Note (1) Response time is measured as the shortest period of time possible between the change in state of an LCD segment as demonstrated below:

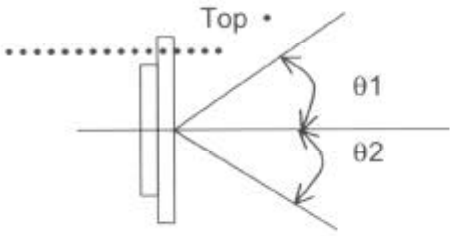


Condition:

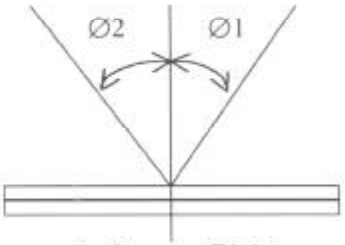
(a). Temperature ----------25°C•••
(b). Frame frequency --------- 70Hz
(c). View Angle ---------- θ = 0°, ∅=0°
(d). Operating voltage -------- 4.5V

Note (2) Definition of View Angle

Top – Bottom direction



Right -- Left direction



7. TIMING CHARACTERISTICS

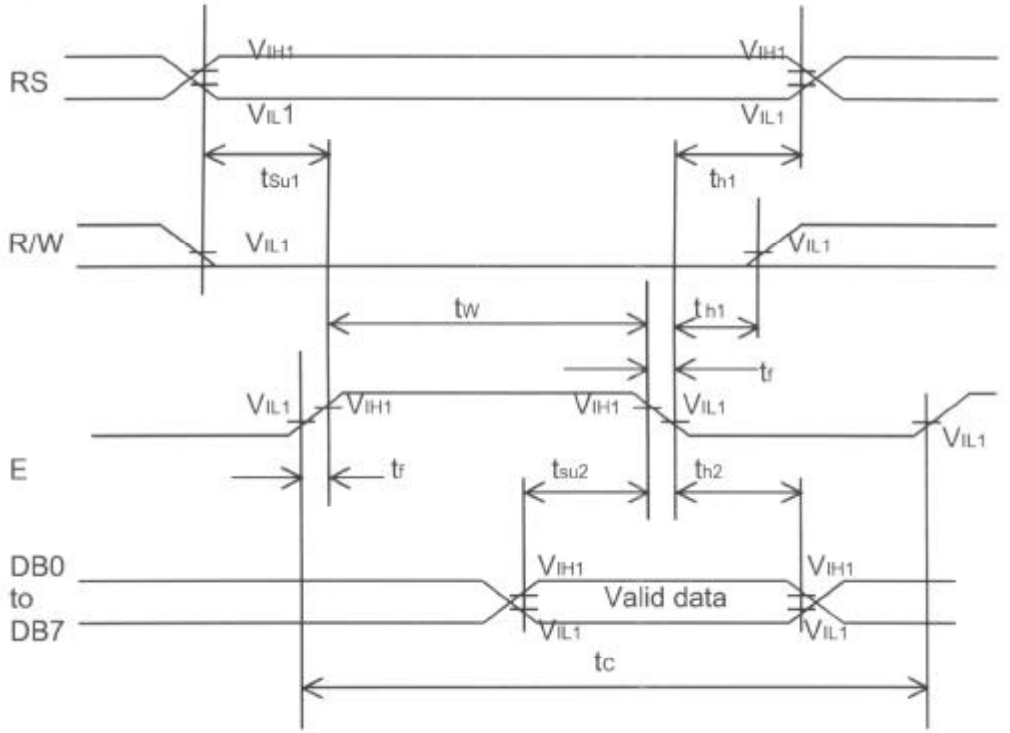
7.1 WRITE TIMING

AC characteristics (VDD=4.5v~5.5v,Ta=-30~85°• ••

Item	Symbol	Condition	Min.	Max.	Unit
E cycle time	tc	VDD = 5V	500	--	ns
E pulse width (high level)	tw		230	--	
E rise/fall time	tR, tF		--	20	
R/W and RS Setup time	tsu1		40	--	
R/W and RS Hold time	tH1		10	--	
Data setup time	tsu2		80	--	
Data hold time	tH2		10	--	

AC characteristics (VDD=2.7v~4.5v,Ta=-30~85°• ••

Item	Symbol	Condition	Min.	Max.	Unit
E cycle time	tc	VDD = 3V	1000	--	ns
E pulse width (high level)	tw		450	--	
E rise/fall time	tR, tF		--	25	
R/W and RS Setup time	tsu1		60	--	
R/W and RS Hold time	tH1		20	--	
Data setup time	tsu2		195	--	
Data hold time	tH2		10	--	



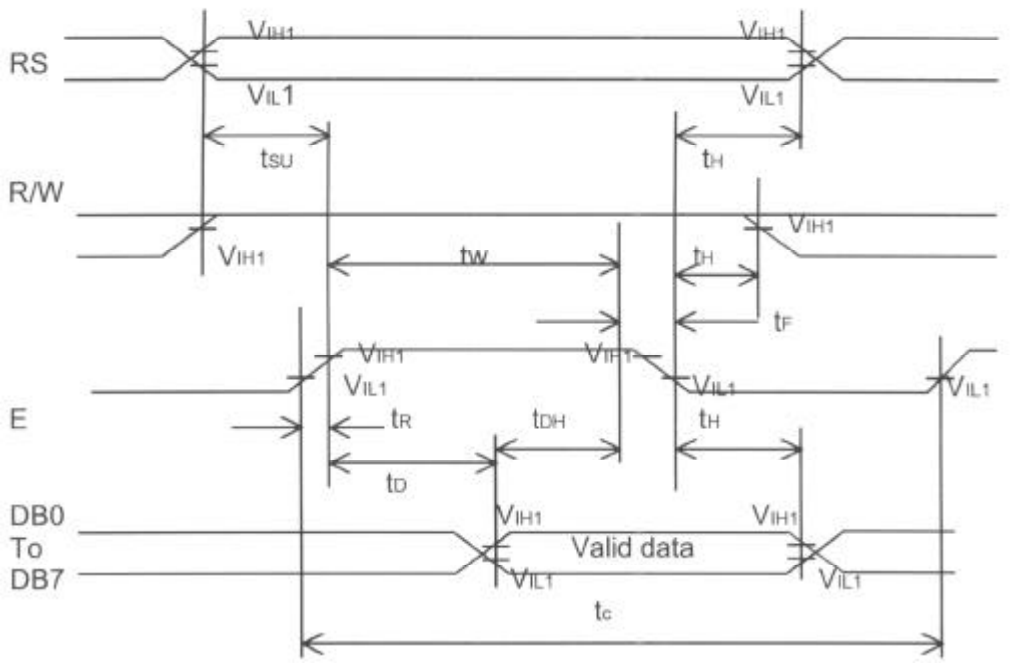
7.2 READ TIMING

AC characteristics (VDD=4.5v~5.5v,Ta=-30~85°• •

Item	Symbol	Condition	Min.	Max.	Unit
E cycle time	tc	VDD = 5V	500	--	ns
E pulse width (high level)	tw		230	--	
E rise/fall time	tR, tF		--	20	
R/W and RS shetup time	tsu		40	--	
R/W and RS hold time	tH		10	--	
Data output delay time	tD		--	120	
Data hold time	tDH		5	--	

AC characteristics (VDD=2.7v~4.5v,Ta=-30~85°• •

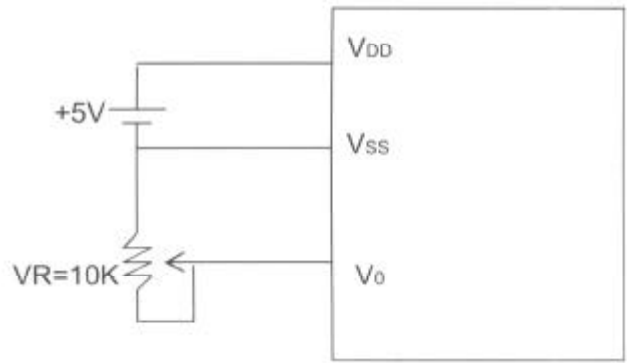
Item	Symbol	Condition	Min.	Max.	Unit
E cycle time	tc	VDD = 3V	1000	--	ns
E pulse width (high level)	tw		450	--	
E rise/fall time	tR, tF		--	25	
R/W and RS setup time	tsu		60	--	
R/W and RS hold time	tH		20	--	
Data output delay time	tD			--	
Data hold time	tDH		10	--	



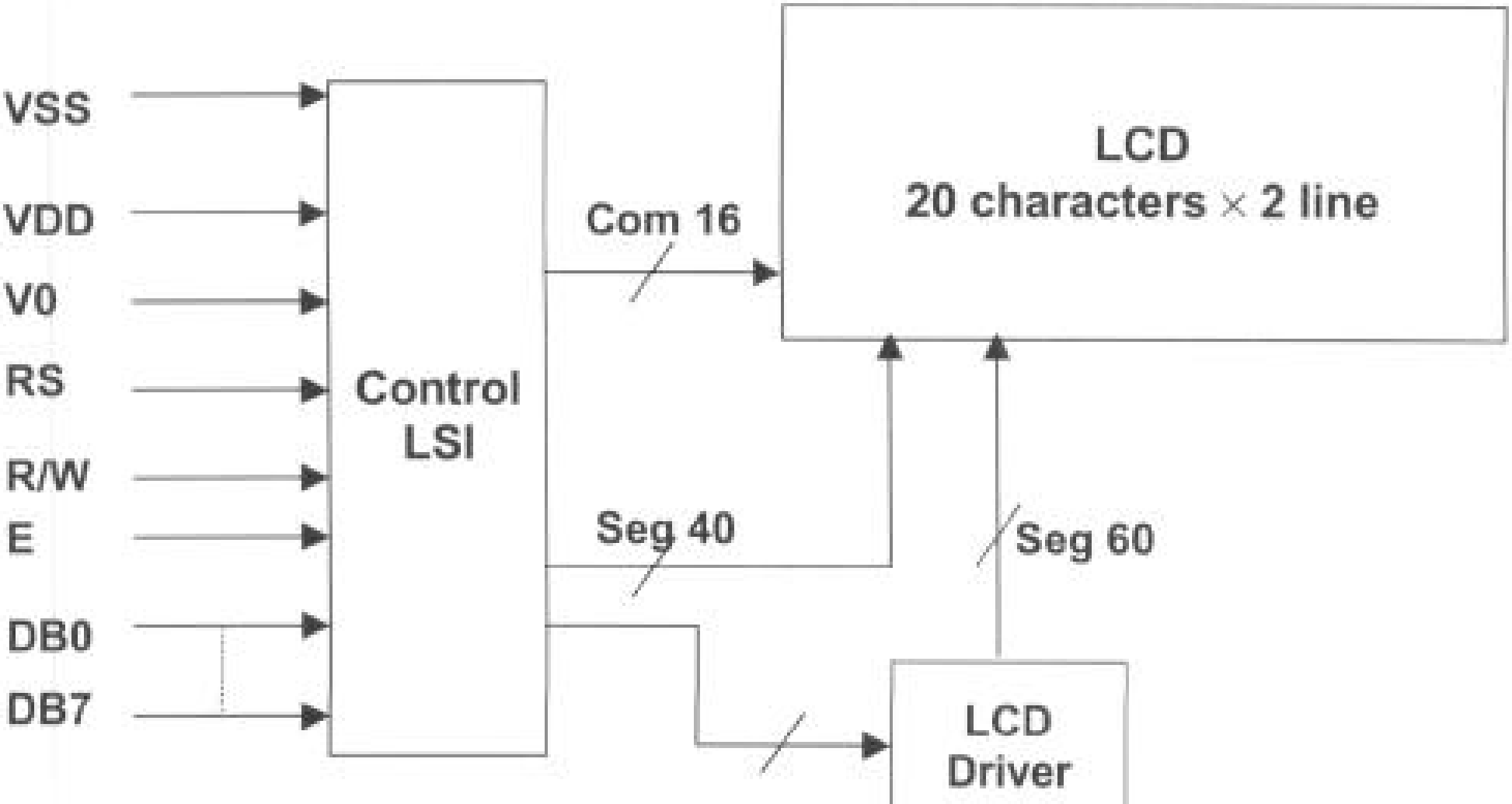
8. PIN CONNECTIONS

No.	Symbol	Function
1	VSS	Ground, 0V
2	VDD	Logic power supply, +5V
3	VO	Voltage for LCD drive
4	RS	Data / Instruction register select
5	R/W	Read / Write
6	E	Enable signal
7	DB0	Data Bus Line
8	DB1	
9	DB2	
10	DB3	
11	DB4	
12	DB5	
13	DB6	
14	DB7	

9. POWER SUPPLY



10. BLOCK DIAGRAM



10.1 INSTRUCTIONS

Instruction	Instruction Code										DESCRIPTION	Executed Time(fosc =270KHz)
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H " from AC	1.53mS
Cursor At Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to "00H" from AC and return cursor to its original Position if shifted. The contents of DDRAM are not changed.	1.53mS
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39μS
Display On/Off Control	0	0	0	0	0	0	1	D	C	B	Set display (D), cursor(C), and Blinking of cursor(B) ON/OFF control bit.	39μS
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shifts cursor bit, and the direction, without changing of DDRAM data.	39μS
Function Set	0	0	0	0	1	DL	N	F	-	-	Sets interface data length (DL:8-BIT/4-BIT), number of display lines(N:2-line/1-line) and, display font type (F:5x11dots/5x8 dots).	39μS
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39μS
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39μS
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0μS
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM / CGRAM)	43μS
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Reads data from internal RAM (DDRAM / CGRAM).	43μS

*"-":don't care

NOTE : When an MPU program with checking the Busy Flag(DB7) is made, it must be necessary 1/2Fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag(DB7)goes to "LOW" .

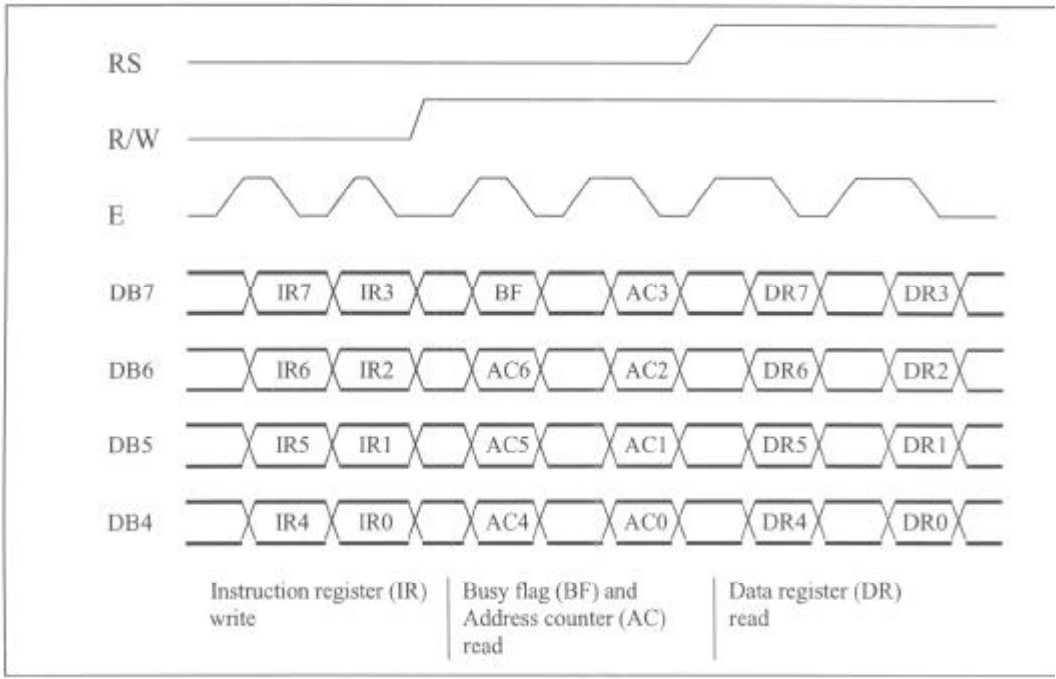
10.2 8-Bit Operation, 8-Digit 2-Line Display Example

Step	Instruction										Display	Operation
No	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
1	Power supply on (the IC is initialized by the Internal reset circuit)											Initialized. No display.
2	Function set											Sets to 8-bit operation and selects 2-line display and 5 8 dot character font.
	0	0	0	0	1	1	1	0				
3	Display on/off control											Turns on display and cursor. All display is in space mode because of initialization.
	0	0	0	0	0	0	1	1	1	0		
4	Entry mode set											Sets mode to increment the address by one and to shift the cursor to the right at the time of write to the DD/CGRAM. Display is not shifted.
	0	0	0	0	0	0	0	1	1	0		
5	Write data to CGRAM/DDRAM										H_	Writes H. DDRAM has already been selected by initialization when the power was turned on. The cursor is incremented by one and shifted to the right
	1	0	0	1	0	0	1	0	0	0		
6												
7	Write data to CGRAM/DDRAM										HITACHI_	Writes I.
	1	0	0	1	0	0	1	0	0	1		
8	Set DDRAM address										HITACHI	Sets DDRAM address so that t The cursor is positioned at the Head of the second lime.
	0	0	1	1	0	0	0	0	0	0		
9	Write data to CGRAM/DDRAM										HITACHI M_	Writes M.
	1	0	0	1	0	0	1	1	0	1		
10												
11	Write data to CGRAM/DDRAM										HITACHI MICROCO_	Writes O.
	1	0	0	1	0	0	1	1	1	1		
12	Entry mode set										HITACHI MICROCO_	Sets mode to shift display at the time of write.
	0	0	0	0	0	0	0	1	1	1		
13	Write data to CGRAM/DDRAM										ITACHI ICROCOM_	Writes M. Display is shifted to the left. The first and second lines both shift at the same time.
	1	0	0	1	0	0	1	1	0	1		
14												
15	Return home										HITACHI MICROCOM	Returns both display and cursor to the original position (address 0).
	0	0	0	0	0	0	0	0	1	0		

10.3 Interfacing to the MPU

The IC can send data in either two 4-bit operations, thus allowing interfacing with 4- or 8-bit MPUs.

- For 4-bit interface data, only four bus lines (DB4 to DB7) are used for transfer. Bus lines DB0 to FB3 are disabled. The data transfer between the IC and the MPU is completed after the 4-bit data has been transferred twice. As for the order of data transfer, the four high order bits (for 8-bit operation,DB4 to DB7) are transferred before the four low order bits (for 8-bit operation, DB0 to DB3).
  The busy flag must be checked (one instruction) after the 4-bit data has been transferred twice. Two more 4-bit operations then transfer the busy flag and address counter data.



4-Bit Transfer Example

10.4 1-Line Display

Display position	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DDRAM Address (hexadecimal)	00	01	02	03	04	05	06	07	40	41	42	43	44	45	46	47

16 1-Line

2-Line Display

Display position	1	2	3	4	5	··8··	··16··	··20··	··24··	39	40
DDRAM Address (hexadecimal)	00	01	02	03	04	.....				26	27
	40	41	42	43	44	.....				66	67

8 2-Line

16 2-Line

20 2-Line

24 2-Line

4-Line Display

Display position	1	2	3	.....	15	16
DDRAM Address (hexadecimal)	00	01	02	.....	0E	0F
	40	41	42		4E	4F
	14	15	16		1E	1F
	54	55	56		5E	5F

16 4 Line

Display position	1	2	3	.....	19	20
DDRAM Address (hexadecimal)	00	01	02	.....	12	13
	40	41	42		52	53
	14	15	16		26	27
	54	55	56		66	67

20 4 Line

10.5 CGRAM

Relationship between CGRAM Addresses, Character Codes (DDRAM) and Patterns (CGRAM Data)

For 5 8 dot character patterns

Character Codes (DDRAM data)	CGRAM Address	Character Patterns (CGRAM data)	
7 6 5 4 3 2 1 0	5 4 3 2 1 0	7 6 5 4 3 2 1 0	
High Low	High Low	High Low	
0 0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 0	Character Pattern (1)
	0 0 1	1 0 0 0 1	
	0 1 0	1 0 0 0 1	
	0 1 1	1 1 1 1 0	
	1 0 0	1 0 1 0 0	
	1 0 1	1 0 0 1 0	
	1 1 0	1 0 0 0 1	
	1 1 1	0 0 0 0 0	Cursor position
0 0 0 0 0 0 1	0 0 0 1	1 0 0 0 1	Character Pattern (2)
	0 0 1	0 1 0 1 0	
	0 1 0	1 1 1 1 1	
	0 1 1	0 0 1 0 0	
	1 0 0	1 1 1 1 1	
	1 0 1	0 0 1 0 0	
	1 1 0	0 0 1 0 0	
	1 1 1	0 0 0 0 0	Cursor position
	0 0 0		
	0 0 1		
0 0 0 0 1 1 1	1 1 1		
	1 0 0		
	1 0 1		
	1 1 0		
	1 1 1		

Notes :

1. Character code bits 0 to 2 correspond to CGRAM address bits 3 to 5 (3 bits: 8 types).
2. CGRAM address bits 0 to 2 designate the character pattern line position. The 8th line is the cursor position and its display is formed by a logical OR with the cursor. Maintain the 8th line data, corresponding to the cursor display position, at 0 as the cursor display.
   If the 8th line data is 1, 1 bits will light up the 8th line regardless of the cursor presence.
3. Character pattern row positions correspond to CGRAM data bits 0 to 4 (bit 4 being at the left).
4. As shown Table 5, CGRAM character patterns are selected when character code bits 4 to 7 are all 0. However, since character code bit 3 has no effect, the R display example above can be selected by either character code 00H or 08H.
5. 1 for CGRAM data corresponds to display selection and 0 to non-selection. Indicates no effect.

10.6 Correspondence between Character Codes and Character Patterns (ROM Code:A00)

Note: The user can specify any pattern for character-generator RAM.

11. QUALITY ASSURANCE

11.1 Test Condition

11.1.1 Temperature and Humidity(Ambient Temperature)

Temperature : 20 ± 5°C
Humidity : 65 ± 5%

11.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

11.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

11.1.4 Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

11.1.5 Test Method

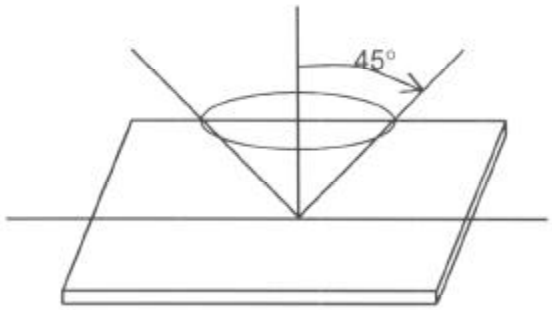
No.	Parameter	Conditions	Regulations
1	High Temperature Operating	50 ± 2 °C	Note 3
2	Low Temperature Operating	0 ± 2 °C	Note 3
3	High Temperature Storage	70 ± 2 °C	Note 3
4	Low Temperature Storage	-20 ± 2 °C	Note 3
5	Vibration Test (Non-operation state)	Total fixed amplitude : 1.5mm Vibration Frequency : 10 ~ 55Hz One cycle 60 seconds to 3 directions of X.Y.Z. for each 15 minutes	Note 3
6	Damp Proof Test (Non-operation state)	40°C ± 2°C, 90~95%RH, 96h	Note 1,2
7	Shock Test (Non-operation state)	To be measured after dropping from 60cm high once concrete surface in packing state	Note 3

Note 1: Returned under normal temperature and humidity for 4 hrs.
Note 2: No dew condensation to be observed.
Note 3: No change on display and in operation under the test condition

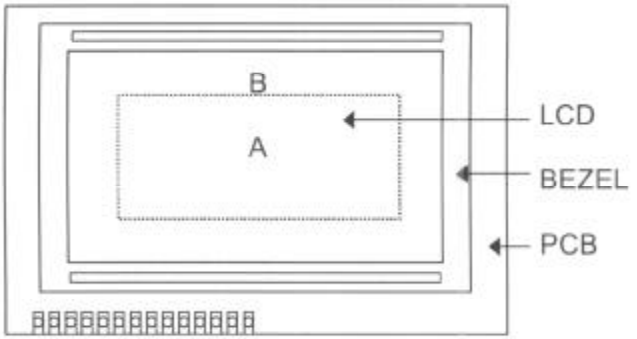
11.2 Inspection condition

11.2.1 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.



11.2.2 Definition of applicable Zones



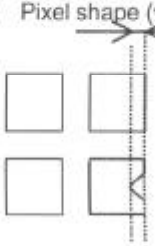
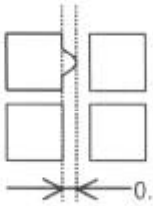
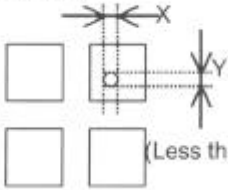
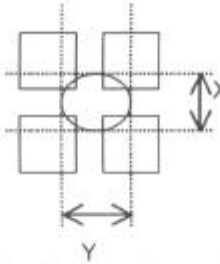
A : Display Area
B : Non-Display Area

11.2.3 Inspection Parameters

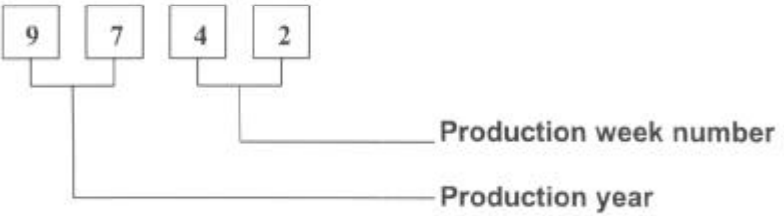
<table>
<tr><th>No.</th><th>Parameter</th><th>Criteria</th></tr>
<tr><td>1</td><td>Black or White spots</td><td>
<table>
<tr><th rowspan="2">Zone / Dimension</th><th colspan="2">Acceptable number</th><th rowspan="2">Class Of Defects</th><th rowspan="2">AQL Level</th></tr>
<tr><th>A</th><th>B</th></tr>
<tr><td>D < 0.15</td><td>*</td><td>*</td><td rowspan="4">Minor</td><td rowspan="4">2.5</td></tr>
<tr><td>0.15≤ D < 0.2</td><td>4</td><td>4</td></tr>
<tr><td>0.2 ≤ D ≤ 0.25</td><td>2</td><td>2</td></tr>
<tr><td>D ≤ 0.3</td><td>0</td><td>1</td></tr>
</table>
D = (Long + Short) / 2 * : Disregard
</td></tr>
<tr><td>2</td><td>Scratch, Substances</td><td>
<table>
<tr><th rowspan="2">X (mm) \ Zone Y(mm)</th><th colspan="2">Acceptable number</th><th rowspan="2">Class Of Defects</th><th rowspan="2">AQL Level</th></tr>
<tr><th>A</th><th>B</th></tr>
<tr><td>*</td><td>0.04 ≥ W</td><td>*</td><td>*</td><td rowspan="4">Minor</td><td rowspan="4">2.5</td></tr>
<tr><td>3.0 ≥ L</td><td>0.06 ≥ W</td><td>4</td><td>4</td></tr>
<tr><td>2.0 ≥ L</td><td>0.08 ≥ W</td><td>2</td><td>3</td></tr>
<tr><td>—</td><td>0.1 < W</td><td>0</td><td>1</td></tr>
</table>
X : Length Y : Width * : Disregard

Total defects should not exceed 4/module
</td></tr>
<tr><td>3</td><td>Air Bubbles
(between glass
& polarizer)</td><td>
<table>
<tr><th rowspan="2">Zone / Dimension</th><th colspan="2">Acceptable number</th><th rowspan="2">Class of Defects</th><th rowspan="2">AQL Level</th></tr>
<tr><th>A</th><th>B</th></tr>
<tr><td>D ≤ 0.15</td><td>*</td><td>*</td><td rowspan="3">Minor</td><td rowspan="3">2.5</td></tr>
<tr><td>0.15 < D ≤ 0.25</td><td>2</td><td>*</td></tr>
<tr><td>0.25 < D</td><td>0</td><td>1</td></tr>
</table>
* : Disregard

Total defects shall not excess 3/module.
</td></tr>
</table>

3	Uniformity of Pixel	(1) Pixel shape (with Dent)  (2) Pixel shape (with Projection)  (3) Pin hole  (4) Deformation  Total acceptable number : 1/pixel, 5/cell

12. LOT NUMBERING SYSTEM



13. LCM NUMBERING SYSTEM

CXM 20211 S R JA

Product code:A

MODE
M – transmissive
Nil – transflective
R – Reflective
N - Negative

LCD type
S - STN Gray
S1 - STN Yellow
N – TN

Series code
20211– 20 characters x 2 lines 11th type

Model type
CM – Character Module
GM – Graphic Module
CXM – Custom Character Module
GXM – Custom Graphic Module

14. PRECAUTION FOR USING LCM

1. LIQUID CRYSTAL DISPLAY (LCD)

LCD is made up of glass, organic sealant, organic fluid, and polymer based polarizers. The following precautions should be taken when handing,

(1). Keep the temperature within range of use and storage. Excessive temperature and humidity could cause polarization degredation, polarizer peel off or bubble.

(2). Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin.

(3). Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.

(4). Glass can be easily chipped or cracked from rough handling, especially at corners and edges.

(5). Do not drive LCD with DC voltage.

2. Liquid Crystal Display Modules

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.

(1). Do not tamper in any way with the tabs on the metal frame.

(2). Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.

(3). Do not touch the elastomer connector, especially insert an backlight panel (for example, EL).

(4). When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting . Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

(5). Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

2.2. Static Electricity

LCM contains CMOS LSI's and the same preca ution for such devices should apply, namely

(1). The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.

(2). The modules should be kept in antistatic bags or other containers resistant to static for storage.

(3). Only properly grounded soldering irons should be used.

(4). If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

(5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

(6). Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

2.3 Soldering

(1). Solder only to the I/O terminals.

(2). Use only soldering irons with proper grounding and no leakage.

(3). Soldering temperature : 280°C ± 10°C

(4). Soldering time: 3 to 4 sec.

(5). Use eutectic solder with resin flux fill.

(6). If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed after wards.

2.4 Operation

(1). The viewing angle can be adjusted by varying the LCD driving voltage V0.

(2). Driving voltage should be kept within specified range; excess voltage shortens display life.

(3). Response time increases with decrease in temperature.

(4). Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".

(5). Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

2.5 Storage

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

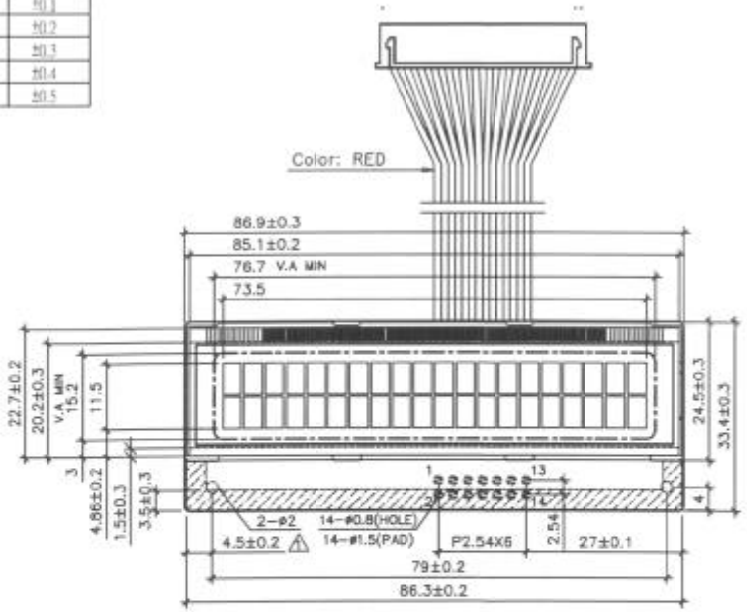
2.6 Limited Warranty

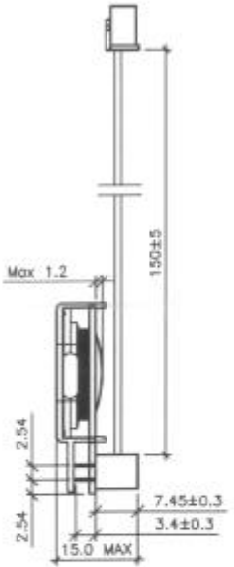
Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not responsible for any subsequent or consequential events.

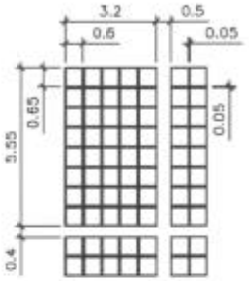
15. OUTLINE DRAWING

. . . . .	
L< 6	±0.05
6< L< 20	±0.1
20< L< 50	±0.2
50< L <180	±0.3
180< L <300	±0.4
300< L	±0.5

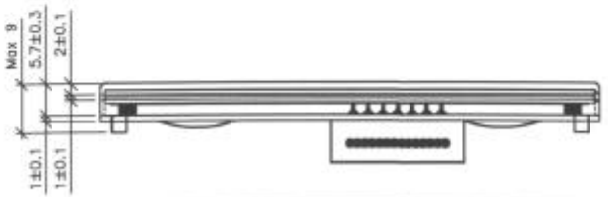
UNITS MM







Detail of Dots
Scale 5/1



→ NO COMPONENT AREA
(SOLDER SIDE)

PIN FUNCTIONS

1	VSS	8	DB1
2	VDD	9	DB2
3	VO	10	DB3
4	R/S	11	DB4
5	R/W	12	DB5
6	E	13	DB6
7	DBO	14	DB7

	DESCRIPTION	DATE	APPROVED
⚠	Change PCB hole dimension ,from 3.5±0.3 to 4.5±0.2	08/28/00	

REVISIONS

ISSUE DATE: 01/11/99
DRAWN:
CHECK:
APPROVE:

DATA IMAGE .....

TITLE: LCM OUTLINE DIMENSION

A4 | DWG. NO. Q990102 | PART. NO. CXM20211 | REV. C

SCALE 1/1 | SHEET 1 OF 1

16. PACKAGE INFORMATION